

- Quick Start Guide (this document)
  - CP21xx Installation DVD
    - Mini-USB cable
- CP2130 evaluation board (pictured)
- The CP2130 Evaluation Kit contains the following:

The CP2130 USB-to-SPI smart-interface family provides a simple solution for connecting Serial Peripheral Interface (SPI) based designs to USB using a minimum of components and PCB space. The CP2130 includes a USB 2.0 full-speed function controller, USB transceiver, internal USB oscillator, integrated voltage regulator, One-Time Programmable ROM, and one SPI.

## CP2130 USB-TO-SPI EVALUATION KIT QUICK-START GUIDE



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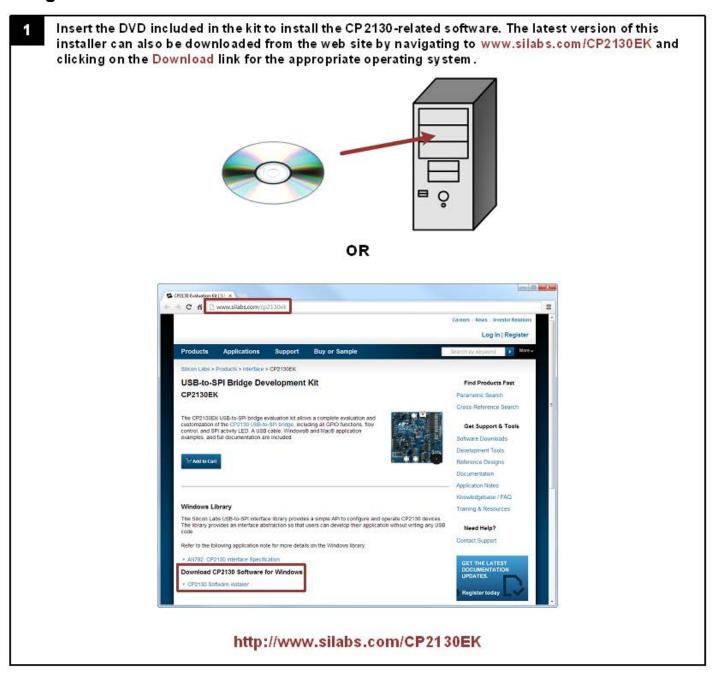
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## A. Getting Started







observe the Event Counter value as it 6. Press the Event button on the board and read the string back from the EEPROM. write text box and press Write. Press Read to 5. Type a string into the SPI EEPROM R/W to turn the GPIO.1 and GPIO.6 LEDs on and 4. Press the Drive Low & Drive High buttons temperature sensor read the ambient temperature from the 3. Observe the Temperature Sensor gauge to observe the Potentiometer Voltage gauge 2. Rotate the potentiometer on the board and 1. Press the Connect button to connect to the In the CP2130\_Demo application:

ADC graph window to display a plot of the

7. Press the Plot ADC button to open the

ADC channels.

Cb5130

Plot ADC racañ caraca 0 PIOLADC (1) athW. beaft 1 SPI EEPROM R/W @ @ Old9 Temperature Sensor ( Connect CP2130 USB-to-SPI Bridge Demo

## B. Relevant Documentation

Application Notes:

www.silabs.com/interface-appnotes

AN721: CP21xx Device Customization Guide

AN792: CP2130 Interface Specification

Device Information:

http://www.silabs.com/smartinterface

http://www.silabs.com/smartinterface—>USB Connectivity Bridges—>CP2130—Documentation tab—Data Sheet section Data Sheets:

http://www.silabs.com/smartinterface→USB Connectivity Bridges→CP2130→Documentation tab→ Users Guides

MCU Knowledge Base:

User Guides section

www.silabs.com→Support→Knowledge Base→USB Bridge Knowledge Base

Contact an Applications Engineer:

www.silabs.com—Support—Contact Technical Support

www.silabs.com/quality Quality Documents:

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moiteframentation CP2130 Evaluation Tool CP2130 Demo MA) Villity (AN CP2130 Software Development Kit

CP2130 Software Development Kit ->

C:/SiLabs/MCU/CP2130\_SDK/Software/

The default installation directory for this software is

Open the CP2130 Demo through the Start menu.

Uninstall CP2130 Software Develo

Silicon Labs

- 🟺 Universal Serial Bus controllers

Jicon Labs CP2130 USB to SPI Bridge

Storage controllers

Processors (TQJ & MOD) shoq 🏰

Metwork adapters

espived BZU abs I nobiliz

Mice and other pointing devices

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package to simplify this process.

Labs provides a DLL with the CP2130 install

the Microsoft WinUSB interface library. Silicon

Windows. The CP2130 can be accessed using

The CP2130 device will appear as a "Silicon

Labs USB Device" in Device Manager in

ellisys protocol analyzers ≥ DVD/CD-ROM drives

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File Action View Help

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CP2130 Demo

Silicon Labs

CP2130\_Demo.

All Programs

4-Jist2

feature called USB selective suspend.

CP2130 EB

CP2130 USB-to-SPI Bridge

external SPI slave device.

3-channel ADC as well as connections for an

evaluation board has an onboard EEPROM and

Connect the CP2130 evaluation board to a PC as

shown using the Mini-USB cable. The CP2130

suspend mode. This is part of a power-saving

no no the ned Suspend LED will then turn on

seconds of inactivity, the green Active LED may

The green Active LED will turn on when the board

as Indicate that the device has entered USB

is properly connected to the PC. After a few